

Cora Z7[™] Statement of Volatility

Revised July 14, 2023 Author: JC This document applies to the Cora Z7 rev. B.X

This document lists the location, purpose, capacity, volatility and (re)programmability of memory devices that may be installed on the Cora Z7 (SKU: 410-370 and 410-370-1 for the XC7Z007S and XC7Z010 part, respectively). The terms programmable and erasable refer to normal means of access available to the public. It does not include reverse-engineering or any other attempts to extract data from these devices.

The content of this document is provided for information purposes only.

Volatile Memory

IC	Memory Purpose	User programmable	User removable	Size	Reset procedure
XC7Z007S-1CLG400C XC7Z010-1CLG400C	Programmable Logic Cells	Yes	No	23360 cells (Z7-07S) 28160 cells (Z7-10)	Remove power for 60 seconds
XC7Z007S-1CLG400C XC7Z010-1CLG400C	Block RAM	Yes	No	1800 Kib (Z7-07S) 2160 Kib (Z7-10)	Remove power for 60 seconds
XC7Z007S-1CLG400C XC7Z010-1CLG400C	L1 Cache	Yes	No	32 KB Instruction and 32 KB data per processor. There is one processor on the -07S, two on the -10.	Remove power for 60 seconds
XC7Z007S-1CLG400C XC7Z010-1CLG400C	L2 Cache	Yes	No	512 KB This is shared between both processors on the -10.	Remove power for 60 seconds
XC7Z007S-1CLG400C XC7Z010-1CLG400C	On-chip Memory	Yes	No	256 KB	Remove power for 60 seconds
MT41K128M16[HA]- 125, MT41K128M16[TW]- 107, A3T4GF40ABF-GML	DDR3 Program / Data memory	Yes	No	4 Gib	Remove power for 60 seconds
RTL8211E-VL-CG	Ethernet Transceiver	Yes	No	N/A	Remove power for 60 seconds
USB3320C-EZK	USB OTG Transceiver	Yes	No	N/A	Remove power for 60 seconds



Non-Volatile Memory

ıc	Memory Purpose	Technology	User programmable	User removable	Size	Reset procedure
93LC56BT-I/OT	USB ID and Configuration	EEPROM	Yes	No	2 KiB	Available through support forum upon request
XC7Z007S-1CLG400C XC7Z010-1CLG400C	On-chip memory BootROM; Factory programmed with boot procedures	N/A	No	No	128 KiB	Non-erasable
XC7Z007S-1CLG400C XC7Z010-1CLG400C	eFUSE Registers for AES decryption, identifiers, and control	eFUSE	Yes, once	No	366 bits	Non-erasable