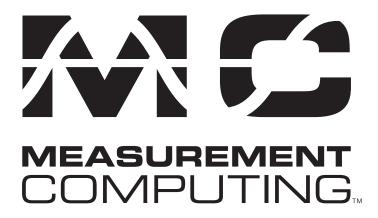
SPECIFICATIONS

CIO-DAS08/JR/16 & CIO-DAS08/JR/16-AO

Analog & Digital I/O



Revision 4, October, 2001
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POWER CONSUMPTION - NO DACS

+5V Supply 310 mA typical / 510 mA max. 25 mA typical / 36 mA max. +12V Supply 16 mA typical / 25 mA max. -12V Supply

POWER CONSUMPTION - WITH DACS

+5V Supply 315 mA typical / 520 mA max. +12V Supply 52 mA typical / 78 mA max. −12V Supply 43 mA typical / 66 mA max.

NOTE: Additional power will be drawn by user's connections to the power pins accessible on

board connectors.

ANALOG INPUTS - Both Versions

TC-850 Integrating Converter Type Resolution 16 bits, 65536 divisions of full scale

Number of Channels 8, single-ended

Range $\pm 5V$

A/D Conversion Time 30 ms Throughput 30 Hz

 \pm 0.5 LSB Max Differential Linearity Error Integral Linearity Error ± 1 LSB Max

No missing codes Guaranteed to 16 bits Voltage Reference Warm Up 20 Minutes Minimum 30 Minutes Typical

Input Leakage Current 100 nA max @ 25 deg. C.

10 Megohms On Channel Impedance

Overvoltage ± 30 Volts Continuous

ANALOG OUTPUTS - CIO-DAS08/JR/16-AO Only

Type AD660

Number of Channels

Range ± 5V Bipolar Only Resolution 16 Bits (1/65536)

Settling time +/-FS $13 \mu s$ Max to $\pm -0.0008\%$ of full scale

Linearity ± 2 LSB

Monotonicity 15 Bits guaranteed over temp range

Offset error +7mV± 3mV Max Gain error Output Current 5 mA Min

Miscellaneous Update DAC channels individually or simultaneously

(jumper-selectable)

Double-buffered latch output

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