Sallen-Key Butterworth Low Pass 4th Order Filter

\[ f_c = 12.5 \text{ kHz} \quad G = 1 \]
\[ f_c = 16.3 \text{ kHz} \quad G = 1 \]

For more information on the parts used in this design, please refer to:

http://www.analog.com/ad8592 (CMOS Single Supply RRO Dual Op Amp with 250 mA Output Current and Shutdown Mode)

http://www.analog.com/ad8515 (1.8 V Low Power CMOS Rail-to-Rail Input/Output Operational Amplifier)

http://www.analog.com/adxl362 (Micropower, 3-Axis Accelerometer)

http://www.analog.com/adt7420 (±0.25°C Accurate, 16-Bit Digital I2C Temperature Sensor)
NOTE: REF_CLK In Mode (ETH_REFCLK = 50MHz)
For more information on the parts used in this design, please refer to:
http://www.analog.com/adr127  (Precision, Micropower LDO Voltage References in TSOT)
<table>
<thead>
<tr>
<th>Doc#</th>
<th>Title</th>
<th>Rev</th>
<th>Copyright</th>
<th>Circuit</th>
<th>Doc#</th>
<th>Engineer</th>
<th>Author</th>
<th>Date</th>
<th>Sheet#</th>
</tr>
</thead>
<tbody>
<tr>
<td>500-292</td>
<td>Nexys A7</td>
<td>D.2</td>
<td>2021</td>
<td>FPGA POWER</td>
<td></td>
<td>4096</td>
<td>11</td>
<td>12/22/2021</td>
<td>10</td>
</tr>
</tbody>
</table>
For more information on the parts used in this design, please refer to:

http://www.analog.com/adp2118  (3 A, 1.2 MHz/600 kHz High Efficiency Synchronous Step-Down DC-to-DC Regulator)

http://www.analog.com/adm1086  (Voltage Sequencer with Active High, Push-Pull Enable Output)

http://www.analog.com/adp2118  (3 A, 1.2 MHz/600 kHz High Efficiency Synchronous Step-Down DC-to-DC Regulator)

http://www.analog.com/adp121  (CMOS Linear Regulator, 150 mA, Low Quiescent Current)