

SPECIFICATIONS

CIO-CTR10

CIO-CTR05

Counter/Timers & Digital I/O



**MEASUREMENT
COMPUTING™**

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Typical for 25°C unless otherwise specified.

POWER CONSUMPTION

+5V	
CIO-CTR10	300 mA typical, 500 mA max
CIO-CTR05	190 mA typical, 320 mA max

COUNTER SECTION

Counter type	9513
Configuration	
CIO-CTR10	Two 9513 devices. 5 counters per 9513, 16 bits each
CIO-CTR05	One 9513 device. 5 counters, 16 bits each
Clock input frequency	7 MHz max
X2 Clock input source	1 MHz (10 MHz Xtal divided by 10)
High pulse width (clock input)	70 ns min
Cycle time (clock input)	145 ns min
Gate pulse duration	145 ns min
Input low voltage	-0.5V min, 0.8V max
Input high voltage	2.2V min, 5V max
Output low voltage	0.4V max @ 3.2mA
Output high voltage	2.4V min @ -200µA
Crystal oscillator clock source	10 MHz
Frequency accuracy	
CIO-CTR##	100 ppm
CIO-CTR##/H50	50 ppm

DIGITAL I/O

Digital Type	
Output:	74LS273
Input:	74LS373
Configuration	2 banks of 8: 1 bank input, 1 bank output, input may be latched via external strobe input (latched on TTL low level)
Number of channels	8 input, 8 output
Output High	2.7 volts min @ -0.4mA
Output Low	0.5 volts max @ 8 mA
Input High	2.0 volts min, 7 volts absolute max
Input Low	0.8 volts max, -0.5 volts absolute min
Interrupts	2 to 7, jumper selectable
Interrupt enable	External, enabled with TTL low level
Interrupt sources	External

ENVIRONMENTAL

Operating temperature range	0 to 50°C
Storage temperature range	-20 to 70°C
Humidity	0 to 90% non-condensing

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