

# **SPECIFICATIONS**

## **CPCI-DIO48H**

## **CPCI-DIO96H**

### High-Output Digital Input/Output



**MEASUREMENT  
COMPUTING™**

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## CPCI-DIO96H

### Power consumption

+5V Operating

CPCI-DIO96H	2.1A typical, 3.5A maximum
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### Digital Input / Output

Digital type	Digital Outputs - 74S244 Digital Inputs - 74LS373
Number of I/O	96
Configuration per 82C55 Emulation	2 banks of 8 bits and 2 banks of 4 bits (82C55A mode 0 emulation) Each port programmable as either input or output
Input low voltage	0.8V maximum
Input high voltage	2.0V minimum
Output low voltage (IOL = 64 mA)	0.5V maximum
Output high voltage (IOH = -15 mA)	2.4V minimum
Absolute maximum input voltages	-0.5V , +5.5V
Power-up / reset state	Input mode (high impedance)
Pull-Up/Pull-Down Resistors	User-installed. Dual footprint allows pull-up or pull-down configuration.

### Environmental

Operating Temperature Range	0 to 70°C
Storage Temperature Range	-40 to 100°C
Humidity	0 to 95% non-condensing

### Mechanical

Card dimensions	+5V 3U CPCI: 160.0mm L x 100.0mmW x 20.3mm H
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## Connector and Pin Out

100 pin Robinson-Nugent.

Compatibility Pinout identical to PCI-DIO96H. Compatible with CIO-DIO96H using C100FF-xx

Pin	Signal Name	Pin	Signal Name
1	Second Port A Bit 7	51	Fourth Port A Bit 7
2	Second Port A Bit 6	52	Fourth Port A Bit 6
3	Second Port A Bit 5	53	Fourth Port A Bit 5
4	Second Port A Bit 4	54	Fourth Port A Bit 4
5	Second Port A Bit 3	55	Fourth Port A Bit 3
6	Second Port A Bit 2	56	Fourth Port A Bit 2
7	Second Port A Bit 1	57	Fourth Port A Bit 1
8	Second Port A Bit 0	58	Fourth Port A Bit 0
9	Second Port B Bit 7	59	Fourth Port B Bit 7
10	Second Port B Bit 6	60	Fourth Port B Bit 6
11	Second Port B Bit 5	61	Fourth Port B Bit 5
12	Second Port B Bit 4	62	Fourth Port B Bit 4
13	Second Port B Bit 3	63	Fourth Port B Bit 3
14	Second Port B Bit 2	64	Fourth Port B Bit 2
15	Second Port B Bit 1	65	Fourth Port B Bit 1
16	Second Port B Bit 0	66	Fourth Port B Bit 0
17	Second Port C Bit 7	67	Fourth Port C Bit 7
18	Second Port C Bit 6	68	Fourth Port C Bit 6
19	Second Port C Bit 5	69	Fourth Port C Bit 5
20	Second Port C Bit 4	70	Fourth Port C Bit 4
21	Second Port C Bit 3	71	Fourth Port C Bit 3
22	Second Port C Bit 2	72	Fourth Port C Bit 2
23	Second Port C Bit 1	73	Fourth Port C Bit 1
24	Second Port C Bit 0	74	Fourth Port C Bit 0
25	First Port A Bit 7	75	Third Port A Bit 7
26	First Port A Bit 6	76	Third Port A Bit 6
27	First Port A Bit 5	77	Third Port A Bit 5
28	First Port A Bit 4	78	Third Port A Bit 4
29	First Port A Bit 3	79	Third Port A Bit 3
30	First Port A Bit 2	80	Third Port A Bit 2
31	First Port A Bit 1	81	Third Port A Bit 1
32	First Port A Bit 0	82	Third Port A Bit 0
33	First Port B Bit 7	83	Third Port B Bit 7
34	First Port B Bit 6	84	Third Port B Bit 6
35	First Port B Bit 5	85	Third Port B Bit 5
36	First Port B Bit 4	86	Third Port B Bit 4
37	First Port B Bit 3	87	Third Port B Bit 3
38	First Port B Bit 2	88	Third Port B Bit 2
39	First Port B Bit 1	89	Third Port B Bit 1
40	First Port B Bit 0	90	Third Port B Bit 0
41	First Port C Bit 7	91	Third Port C Bit 7
42	First Port C Bit 6	92	Third Port C Bit 6
43	First Port C Bit 5	93	Third Port C Bit 5
44	First Port C Bit 4	94	Third Port C Bit 4
45	First Port C Bit 3	95	Third Port C Bit 3
46	First Port C Bit 2	96	Third Port C Bit 2
47	First Port C Bit 1	97	Third Port C Bit 1
48	First Port C Bit 0	98	Third Port C Bit 0
49	+5V	99	+5V
50	GND	100	GND

## CPCI-DIO48H

### Power consumption

+5V Operating

CPCI-DIO48H	1.2 A typical, 1.6 A maximum
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### Digital Input / Output

Digital type	Digital Outputs - 74S244 Digital Inputs - 74LS373
Number of I/O	48
Configuration per 8255 Emulation	2 banks of 8 bits and 2 banks of 4 bits (8255A mode 0 emulation) Each bank programmable as either input or output
Input low voltage	0.8V maximum
Input high voltage	2.0V minimum
Output low voltage (IOL = 64mA)	0.5V maximum
Output high voltage (IOH = -15mA)	2.4V minimum
Absolute maximum input voltage	-0.5V , +5.5V
Power-up / reset state	Input mode (high impedance)
Pull-Up/Pull-Down Resistors	User installed. Dual footprint allows pull-up or pull-down configuration.

### Environmental

Operating Temperature Range	0 to 70°C
Storage Temperature Range	-40 to 100°C
Humidity	0 to 95% non-condensing

### Mechanical

Card dimensions	+5V 3U CPCI: 160.0mmL x 100.0mmW x 20.3mmH
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## Connector and Pin Out

100-pin Robinson-Nugent.

### Compatibility

Pinout is compatible with PCI-DIO48H using Pins 1 to 50 of C100FF-xx.  
Pins 51 to 100 are N/C

Pin	Signal Name	Pin	Signal Name
1	Second Port A Bit 7	51	n/c
2	Second Port A Bit 6	52	n/c
3	Second Port A Bit 5	53	n/c
4	Second Port A Bit 4	54	n/c
5	Second Port A Bit 3	55	n/c
6	Second Port A Bit 2	56	n/c
7	Second Port A Bit 1	57	n/c
8	Second Port A Bit 0	58	n/c
9	Second Port B Bit 7	59	n/c
10	Second Port B Bit 6	60	n/c
11	Second Port B Bit 5	61	n/c
12	Second Port B Bit 4	62	n/c
13	Second Port B Bit 3	63	n/c
14	Second Port B Bit 2	64	n/c
15	Second Port B Bit 1	65	n/c
16	Second Port B Bit 0	66	n/c
17	Second Port C Bit 7	67	n/c
18	Second Port C Bit 6	68	n/c
19	Second Port C Bit 5	69	n/c
20	Second Port C Bit 4	70	n/c
21	Second Port C Bit 3	71	n/c
22	Second Port C Bit 2	72	n/c
23	Second Port C Bit 1	73	n/c
24	Second Port C Bit 0	74	n/c
25	First Port A Bit 7	75	n/c
26	First Port A Bit 6	76	n/c
27	First Port A Bit 5	77	n/c
28	First Port A Bit 4	78	n/c
29	First Port A Bit 3	79	n/c
30	First Port A Bit 2	80	n/c
31	First Port A Bit 1	81	n/c
32	First Port A Bit 0	82	n/c
33	First Port B Bit 7	83	n/c
34	First Port B Bit 6	84	n/c
35	First Port B Bit 5	85	n/c
36	First Port B Bit 4	86	n/c
37	First Port B Bit 3	87	n/c
38	First Port B Bit 2	88	n/c
39	First Port B Bit 1	89	n/c
40	First Port B Bit 0	90	n/c
41	First Port C Bit 7	91	n/c
42	First Port C Bit 6	92	n/c
43	First Port C Bit 5	93	n/c
44	First Port C Bit 4	94	n/c
45	First Port C Bit 3	95	n/c
46	First Port C Bit 2	96	n/c
47	First Port C Bit 1	97	n/c
48	First Port C Bit 0	98	n/c
49	+5V	99	+5V
50	GND	100	GND

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